

**COMBINED DECLARATION AND POWER OF ATTORNEY  
FOR PATENT APPLICATION**

Attorney Docket ONS00030

As a below named inventor, I hereby declare that:

My residence, post office address and citizenship are as stated below next to my name.

I believe I am the original, first and sole inventor (if only one name is listed below), or an original, first and joint inventor (if plural names are listed below), of the subject matter which is claimed and for which a patent is sought on the invention entitled LOW VOLTAGE OUTPUT DRIVE CIRCUIT, the specification of which is attached hereto unless the following box is checked:

☐ Application was filed on \_\_\_\_\_  
as Application No. \_\_\_\_\_  
and was amended on \_\_\_\_\_

I hereby state that I have reviewed and understand the contents of the above identified specification, including the claims, as amended by any amendment referred to above.

I acknowledge the duty to disclose information which is material to the examination of this application in accordance with Title 37, Code of Federal Regulations, §1.56.

I hereby claim foreign priority benefits under Title 35, United States Code, § 119(a)-(d) of any foreign application(s) for patent or inventor's certificate listed below and have also identified below any foreign application for patent or inventor's certificate having a filing date before that of the application on which priority is claimed.

Prior Foreign Application(s)		Priority Claimed
_____ (Number)	_____ (Country)	_____ (Day/Month/Year Filed) <input type="checkbox"/> Yes <input type="checkbox"/> No
_____ (Number)	_____ (Country)	_____ (Day/Month/Year Filed) <input type="checkbox"/> Yes <input type="checkbox"/> No

I hereby claim the benefit under Title 35, United States Code, § 119(e) of any United States provisional application(s) listed below.

_____ (Application Number)	_____ (Filing Date)
_____ (Application Number)	_____ (Filing Date)

I hereby claim the benefit under Title 35, United States Code, § 120 of any United States application(s) listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States application in the manner provided by the first paragraph of Title 35, United States Code, § 112, I acknowledge the duty to disclose information which is material to patentability as defined in Title 37, Code of Federal

Regulations, § 1.56 which became available between the filing date of the prior application and the national or PCT international filing date of this application.

(Application Number)	(Filing Date)	(Status - patented, pending, abandoned)
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(Application Number)	(Filing Date)	(Status - patented, pending, abandoned)
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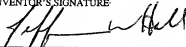
I hereby appoint the following attorney(s) and/or agent(s) to prosecute this application and to transact all business in the U.S. Patent and Trademark Office connected therewith:

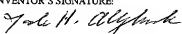
Robert D. Atkins, Reg. No. 34,288.


Address all telephone calls to Mr. Robert D. Atkins at telephone no. (602) 244-3419.

Address all correspondence to Robert D. Atkins, Semiconductor Components Industries, LLC, Patent Administration Dept - MD A230, 5005 East McDowell Road, Phoenix, AZ 85008.

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

FULL NAME OF FIRST INVENTOR: FIRST MIDDLE LAST	INVENTOR'S SIGNATURE	DATE
Jefferson W. Hall		5/5/00
RESIDENCE	CITIZENSHIP:	
4526 East Dry Creek Road, Phoenix, AZ 85044	United States	
POST OFFICE ADDRESS:		
Same as above		

FULL NAME OF SECOND INVENTOR: FIRST MIDDLE LAST	INVENTOR'S SIGNATURE	DATE
Jade H. Alberkrack		5/5/00
RESIDENCE	CITIZENSHIP:	
1834 East Calle de Caballos, Tempe, AZ 85284	United States	
POST OFFICE ADDRESS:		
Same as above		

FULL NAME OF THIRD INVENTOR: FIRST MIDDLE LAST			INVENTOR'S SIGNATURE: 	DATE: 5/12/2000
Kazuo Ito				
RESIDENCE: 2-6-30 Dainohara Aoba-Ku, Sendai, Japan 981-0911			CITIZENSHIP: Japan	
POST OFFICE ADDRESS: Same as above				

COMBINED DECLARATION AND POWER OF ATTORNEY  
FOR PATENT APPLICATION

As a below named inventor, I hereby declare that:

I believe I am an original and first inventor of the subject matter which is claimed and for which a patent is sought on the invention entitled LOW VOLTAGE OUTPUT DRIVE CIRCUIT under Attorney Docket No. ONS00030P in the Nonprovisional Utility Patent Application that was filed on May 30, 2000 as Nonprovisional Utility Patent Application Serial No. 09/580,560 and was amended on March 1, 2002.

The following persons are named as the inventors in the Nonprovisional Utility Patent Application:

Jefferson W. Hall, Jade H. Alberkrack.

I hereby state that I have reviewed and understand the contents of the above identified Patent Application, including the claims, and further including any amendment referred to above.

I acknowledge the duty to disclose to the US Patent and Trademark Office all information which is known to me to be material to the patentability of the subject Patent Application in accordance with Title 37, Code of Federal Regulations, §1.56.

I appoint the following attorney(s) and/or agent(s) of record to prosecute this Patent Application and to transact all business in the U.S. Patent and Trademark Office connected therewith:

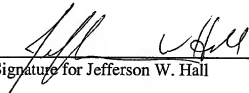
James J. Stipanuk, Reg. No. 44,358, having correspondence address of ON Semiconductor, Patent Administration Dept - MD A700, P.O. Box 62890, Phoenix, AZ 85082-2890, telephone no. (602) 244-5603, fax no. (602) 244-3169.

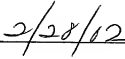
Domestic Priority Information

I hereby claim the benefit under Title 35, United States Code, § 120 of United States patent application(s) listed below and, insofar as the subject matter of each of the claims of this Patent Application is not disclosed in the prior United States patent application in the manner provided by the first paragraph of Title 35, United States Code, § 112, I acknowledge the duty to disclose information which is material to patentability as defined in Title 37, Code of Federal Regulations, § 1.56 which became available between the filing date of the prior application and the national or PCT international filing date of this Patent Application.

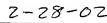
<u>09/580,560</u>	<u>May 30, 2000</u>	<u>pending</u>
Application Number	Filing Date	Status

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the Patent Application or any patent issued thereon.

  
\_\_\_\_\_  
Signature for Jefferson W. Hall

  
\_\_\_\_\_  
Date

  
\_\_\_\_\_  
Signature for Jade H. Alberkrack

  
\_\_\_\_\_  
Date